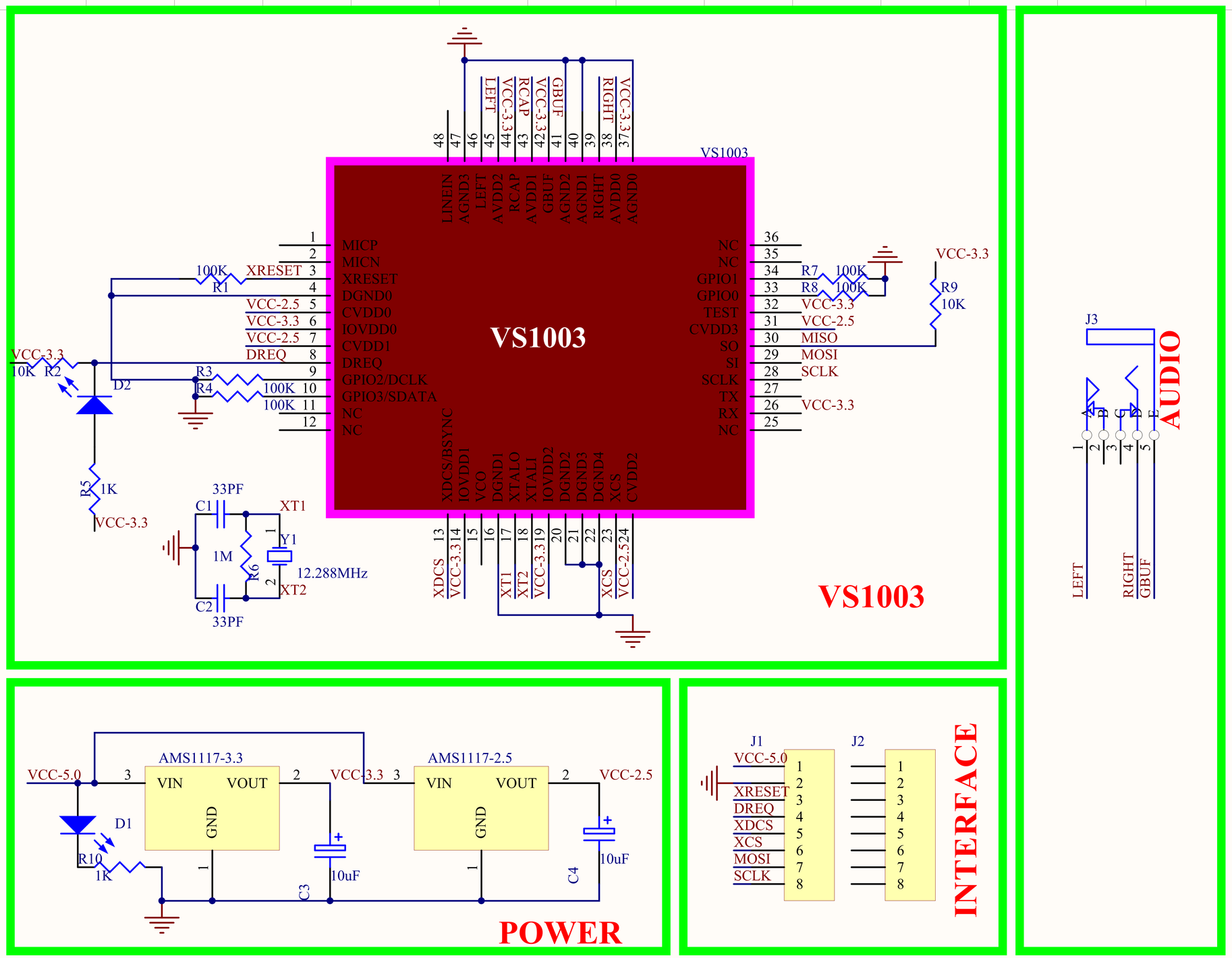
VS1003
VS1003
MICP
MICN
XRESET
DGND0
CVDD0
IOVDD0
CVDD1
DREQ
GPIO2/DCLK
GPIO3/SDATA
NC
NC
LINEIN
AGND3
LEFT
AVDD2
RCAP
AVDD1
GBUF
AGND2
AGND1
RIGHT
AVDD0
AGND0
NC
NC
GPIO1
GPIO0
TEST
CVDD3
SO
SI
SCLK
TX
RX
NC
XDCS/BSYNC
IOVDD1
VCO
DGND1
XTALO
XTALI
IOVDD2
DGND2
DGND3
DGND4
XCS
CVDD2
XDCS
VCC-3.314
XT1
XT2
VCC-3.319
XCS
VCC-2.524
VCC-3.3
VCC-2.5
DREQ
VCC-5.0
R1 100K
R2 10K
R3
R4 100K
R5 1K
R6 1M
R7 100K
R8 100K
R9 10K
D2
C1 33PF
C2 33PF
Y1 12.288MHz
XT1
XT2
MISO
MOSI
SCLK
J3
AUDIO
LEFT
RIGHT
GBUF
AMS1117-3.3
AMS1117-2.5
VIN
VOUT
GND
D1
R10 1K
C3 10uF
C4 10uF
POWER
J1
J2
INTERFACE
Title
Size
E
Number
Revision
Date: 2008-12-31
Sheet of
File: E:\ \..\MP3.SCHDOC
Drawn By: